

1/12

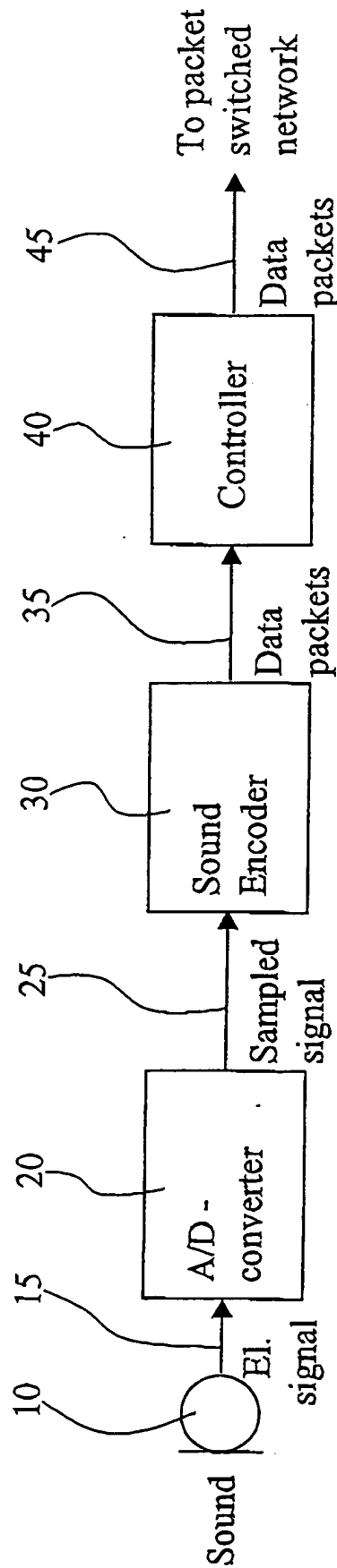


FIG. 1

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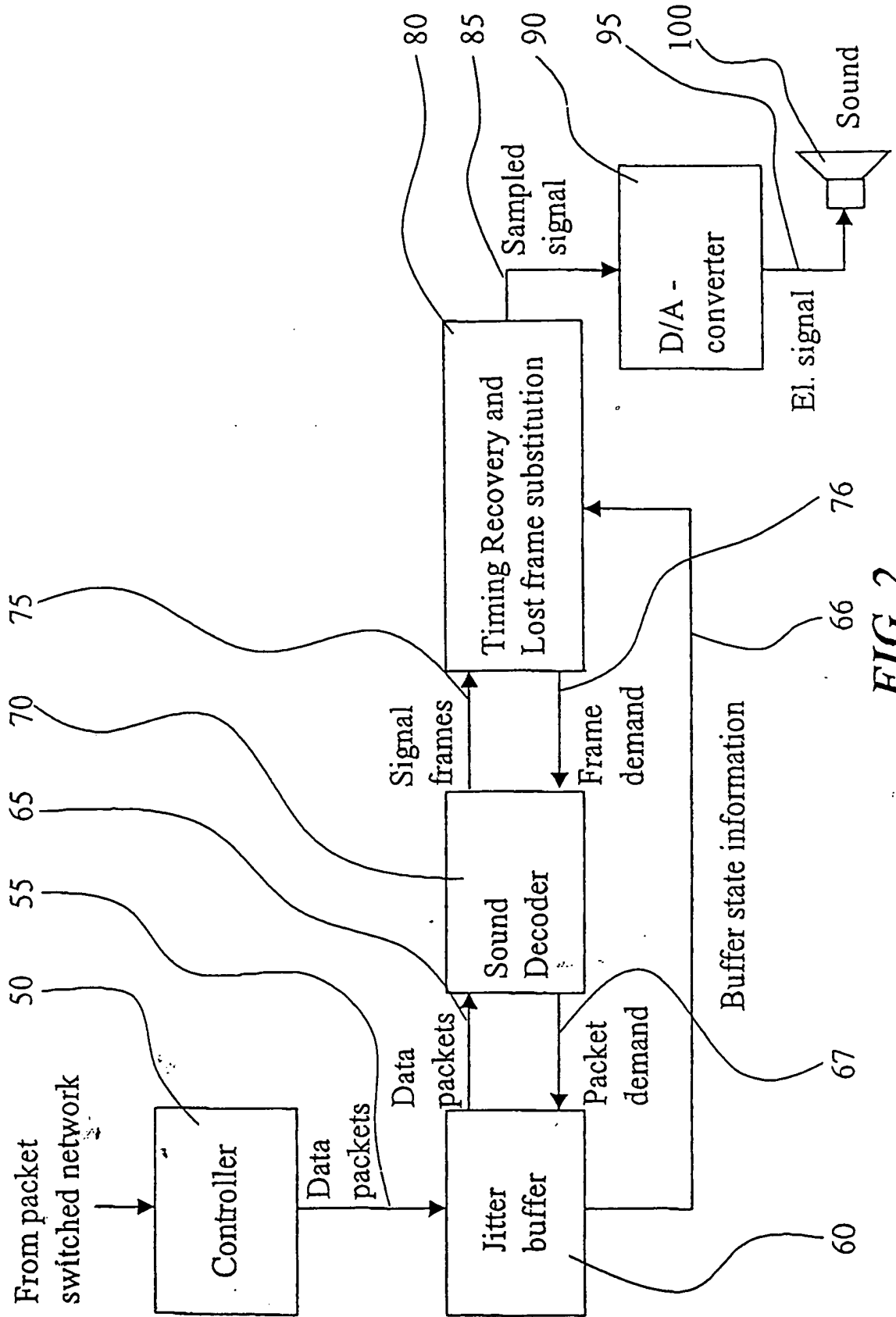


FIG. 2

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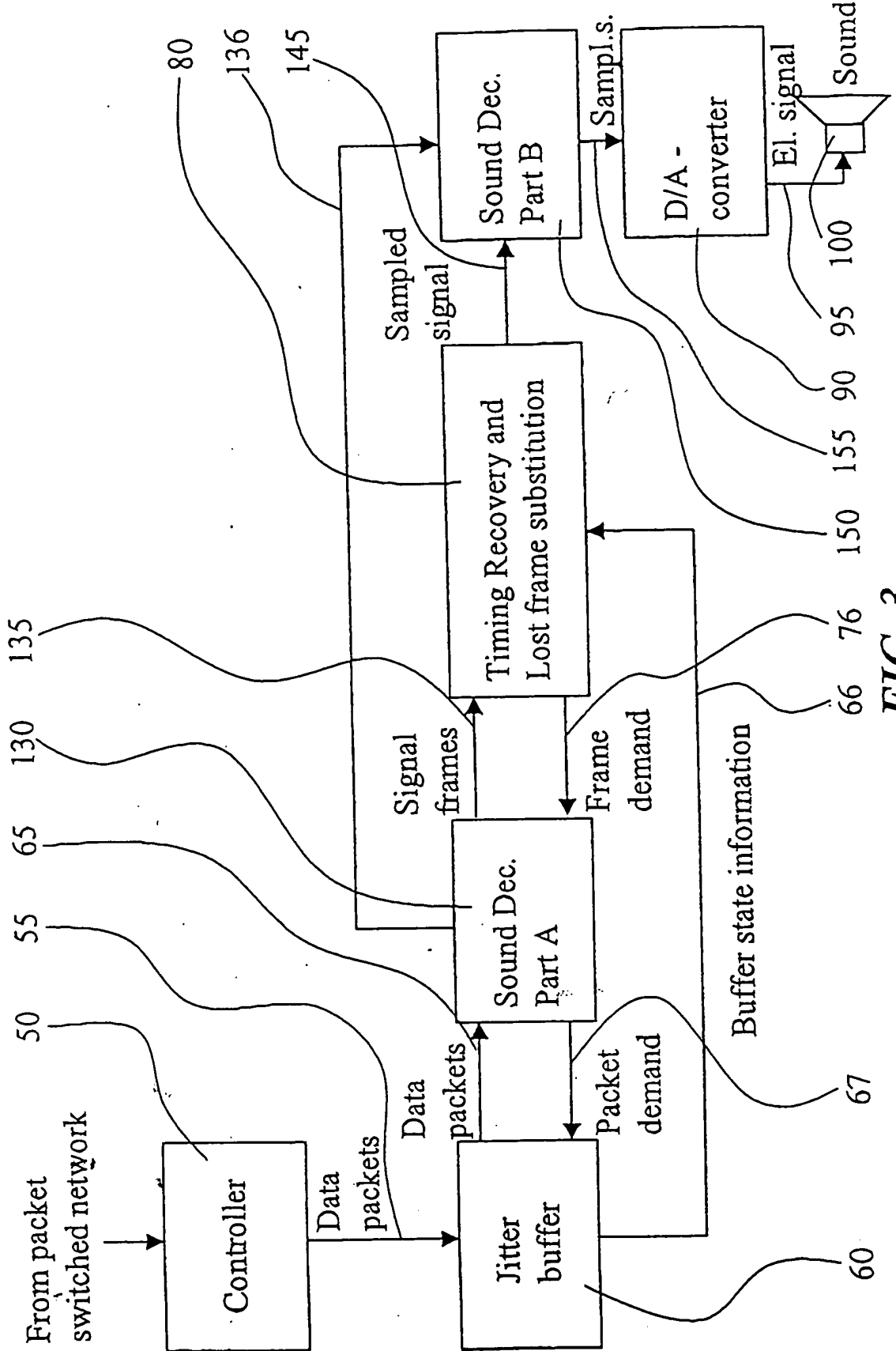


FIG. 3

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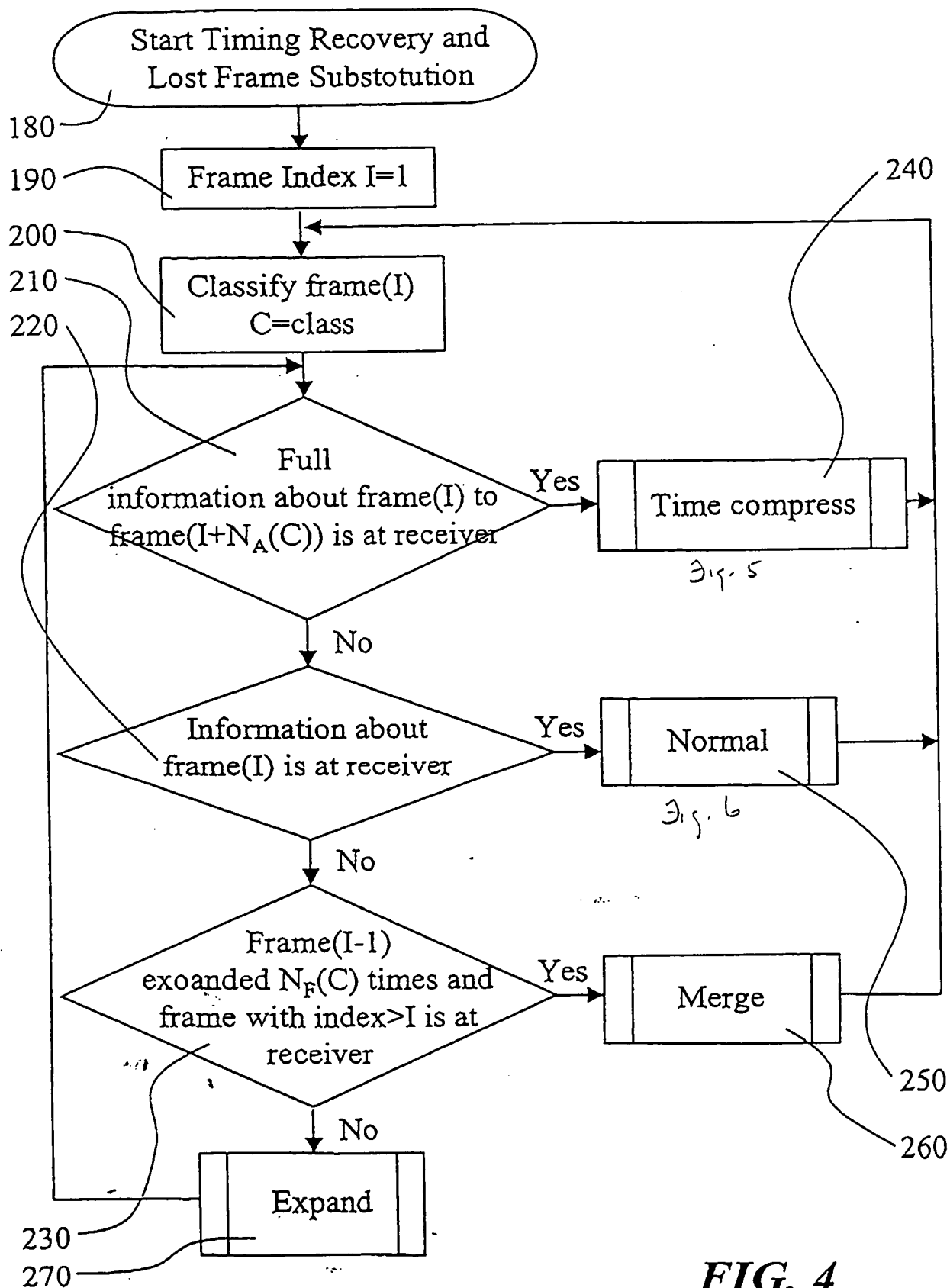
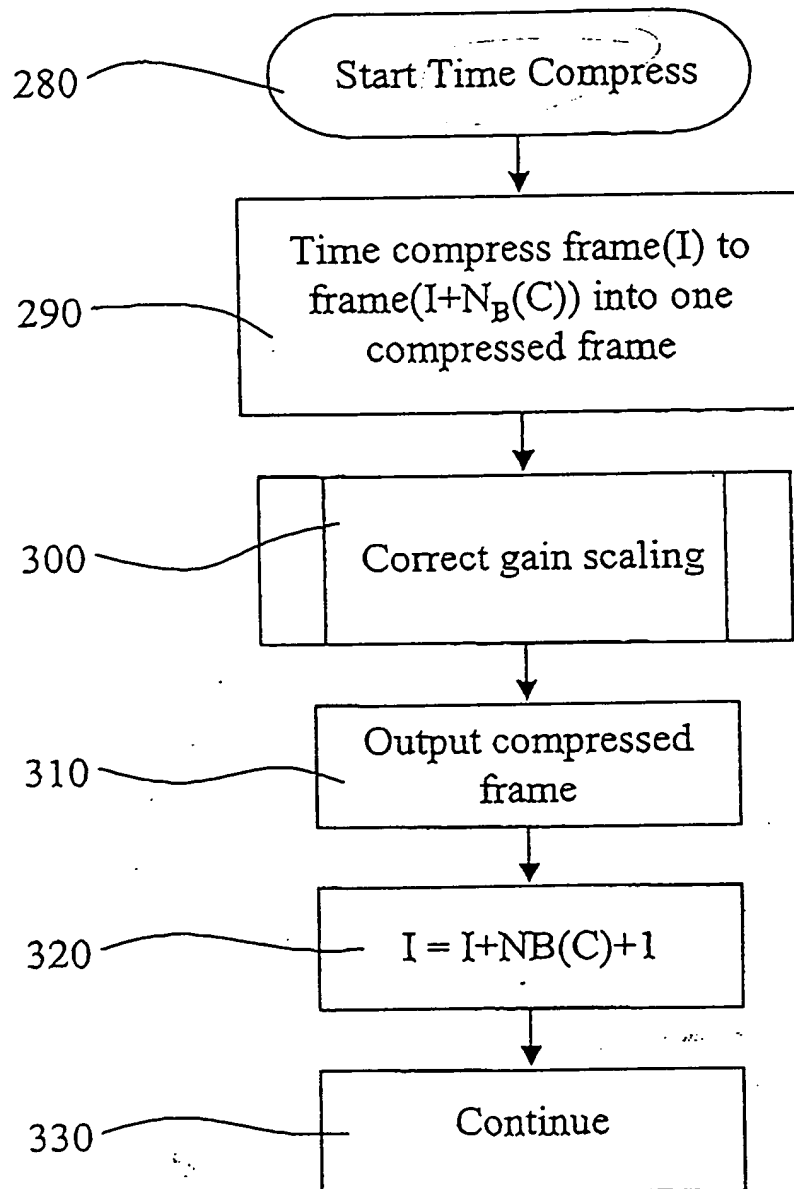


FIG. 4

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**FIG. 5**

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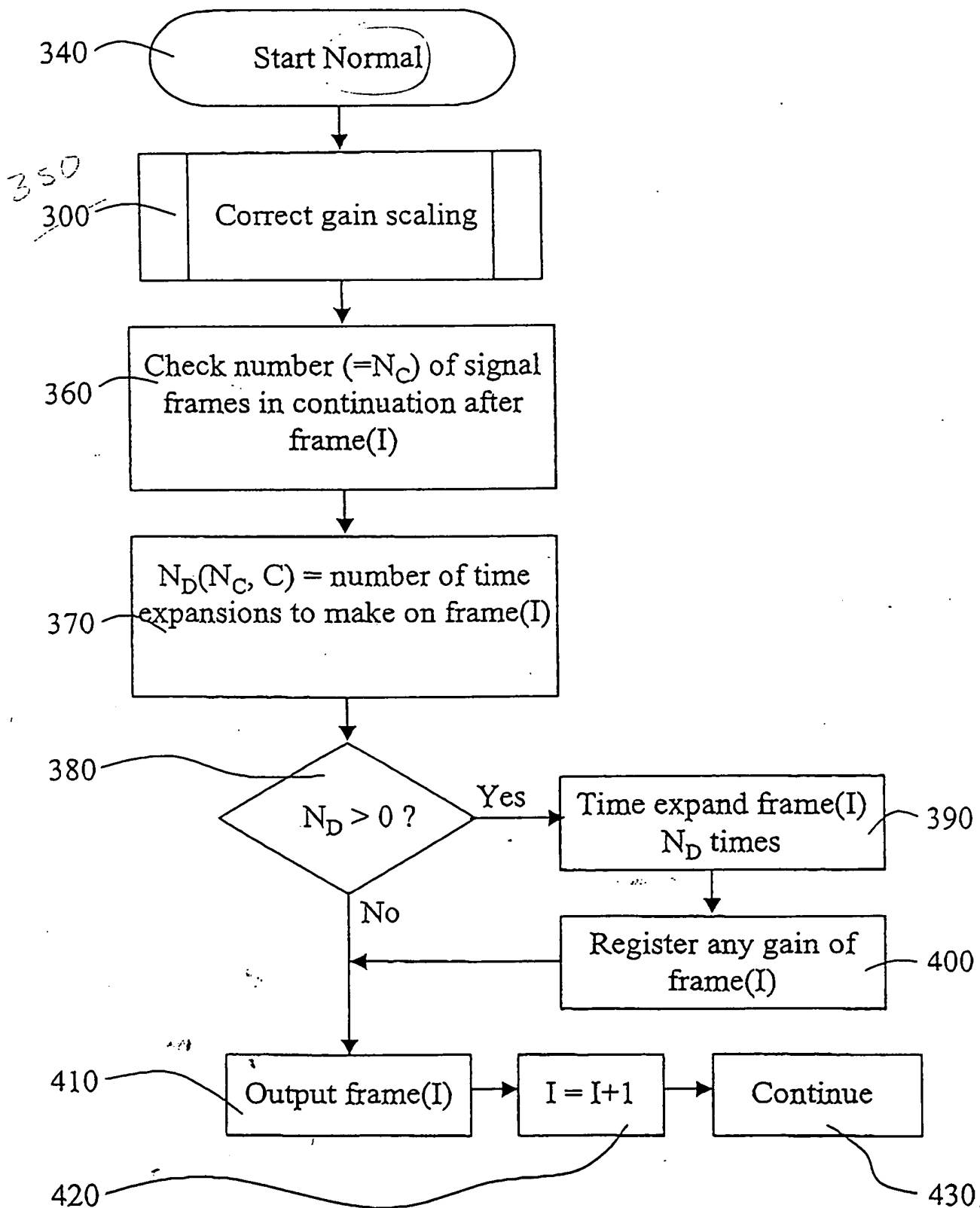
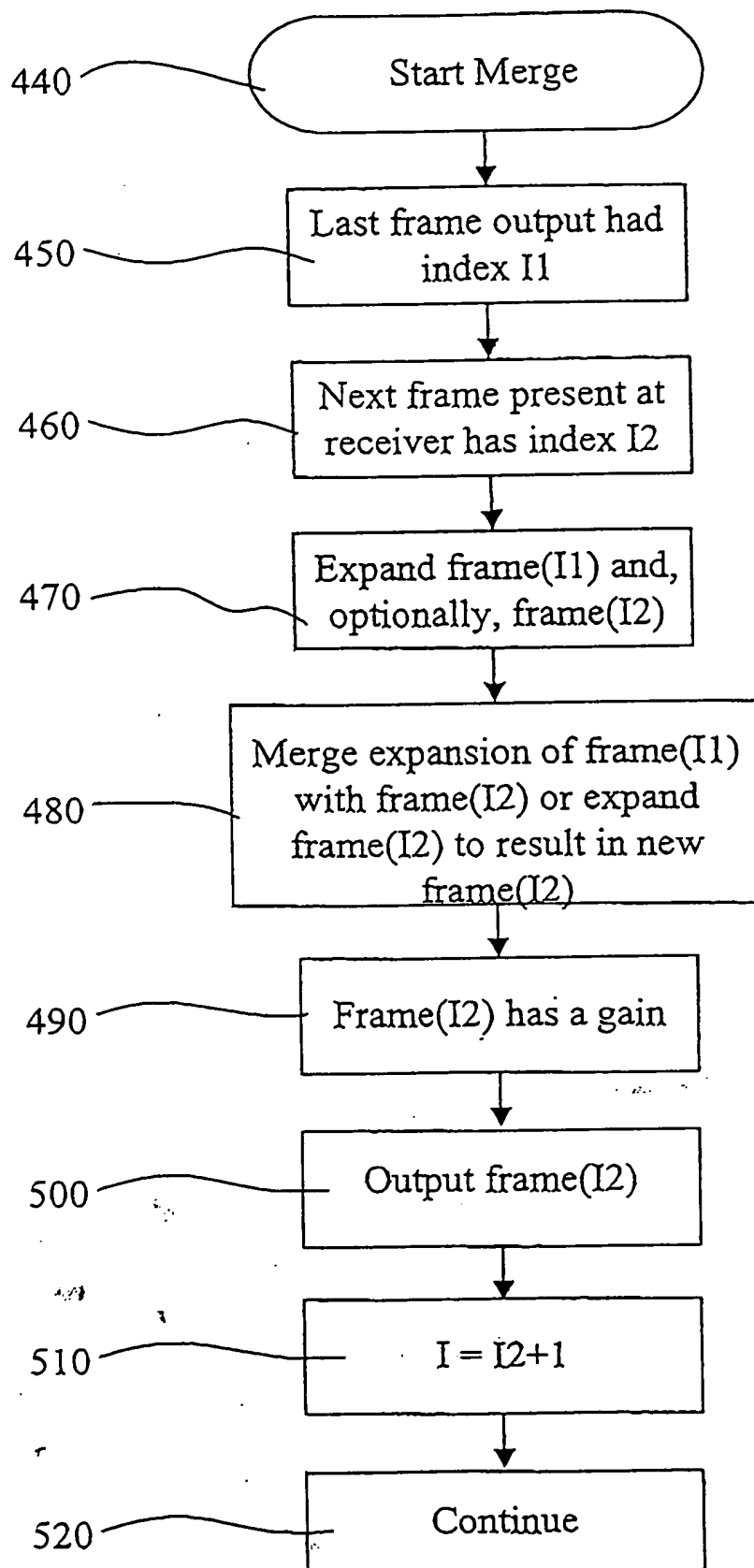
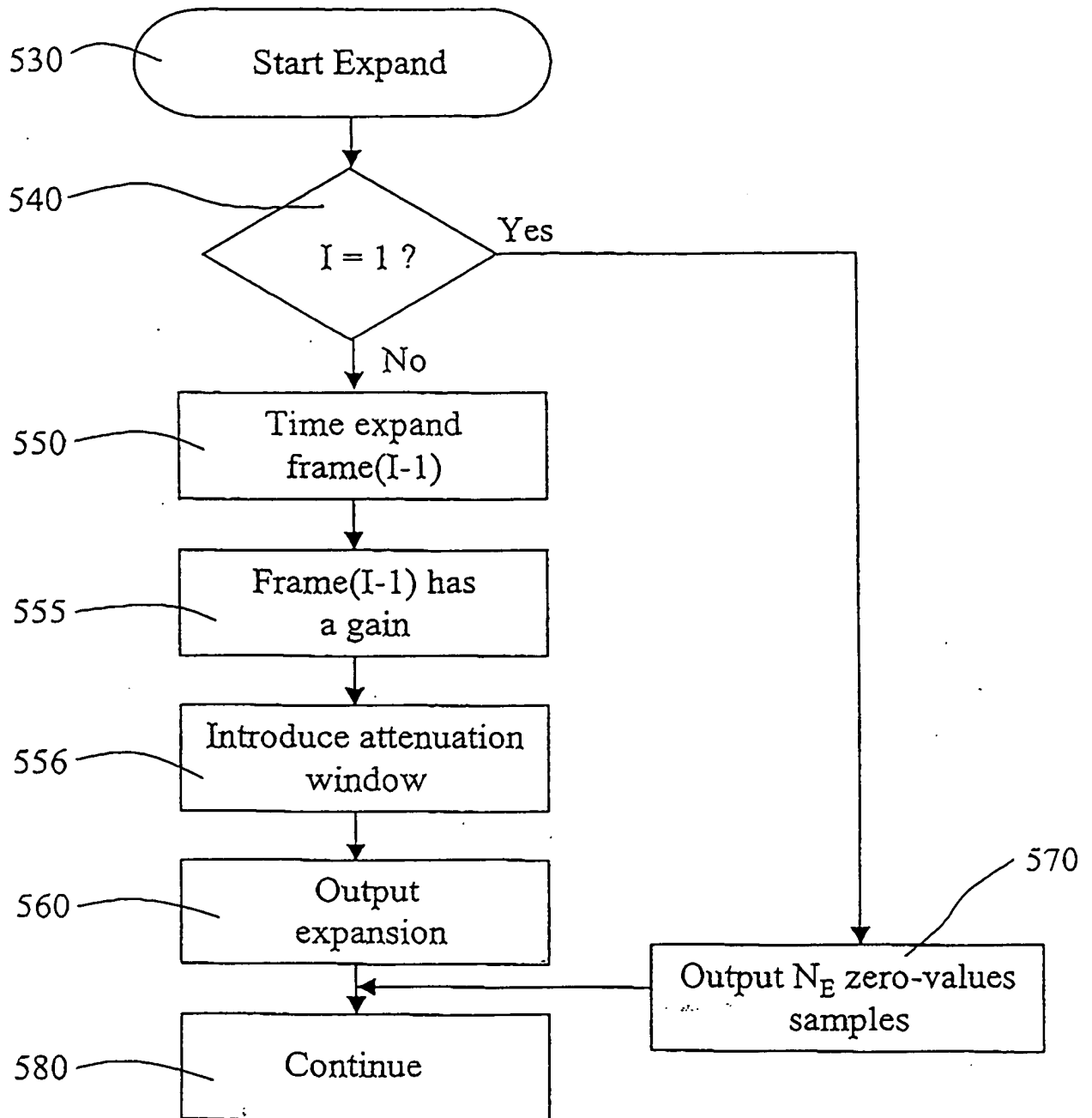


FIG. 6

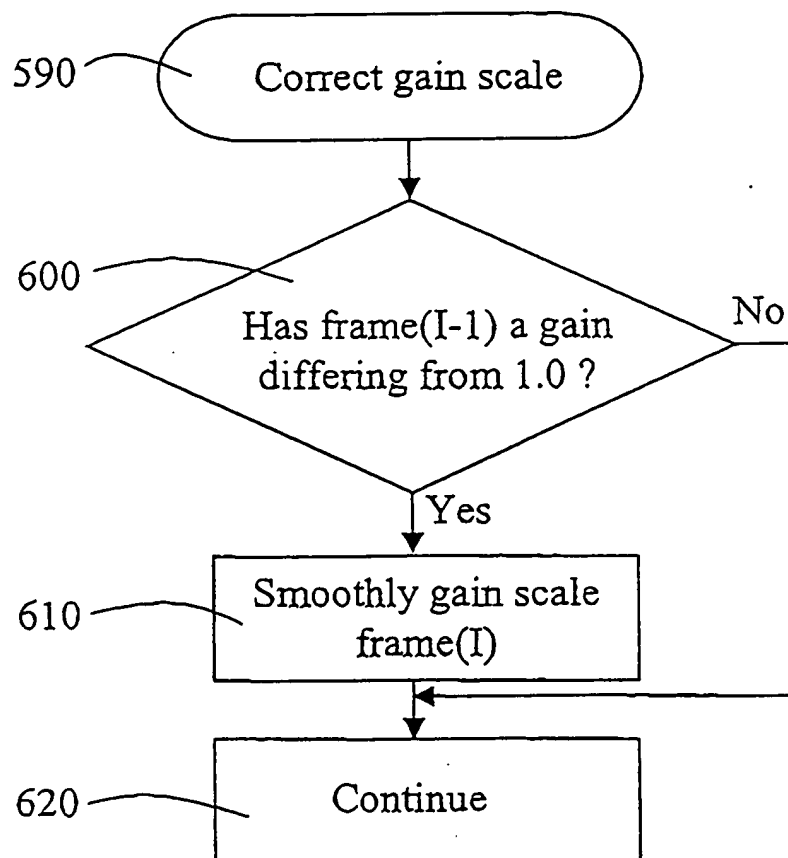
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**FIG. 7**

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**FIG. 8**

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**FIG. 9**

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FIG. 10a

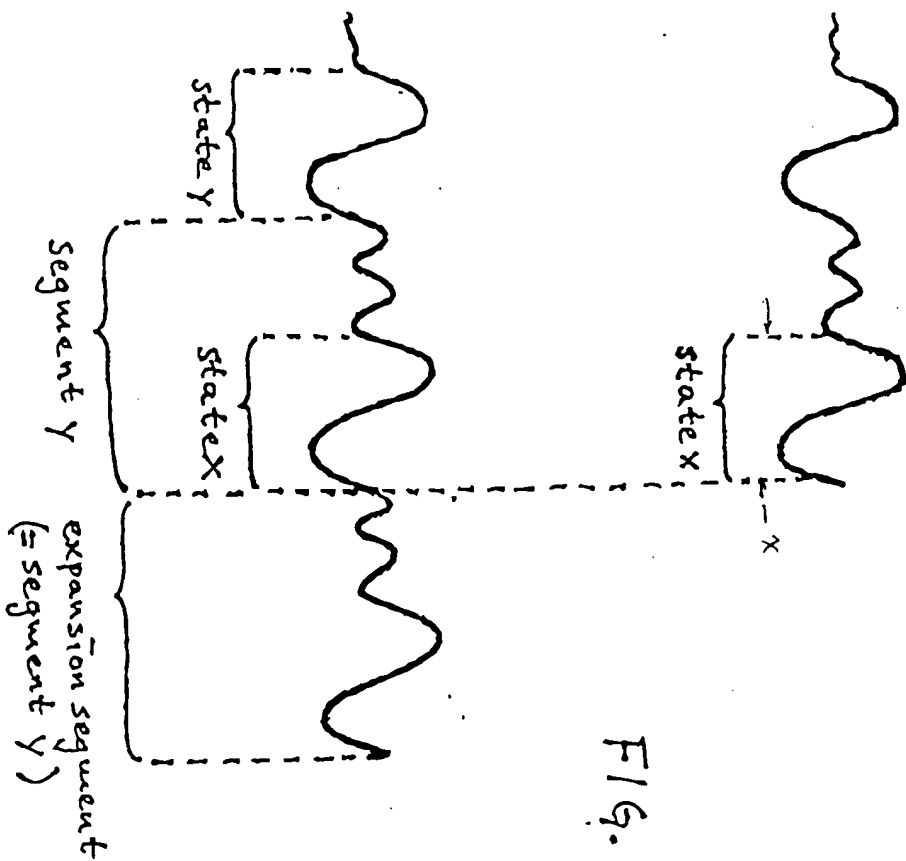
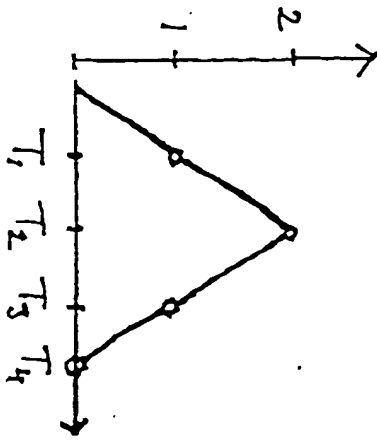


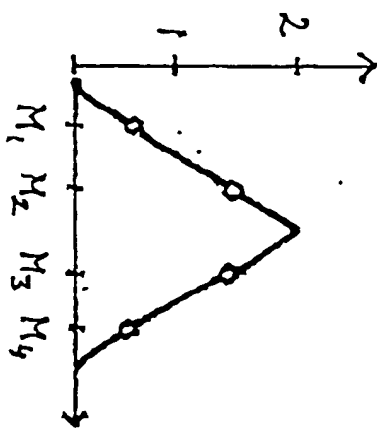
FIG. 10b

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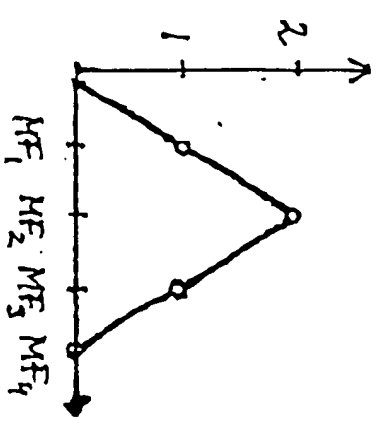
True state

FIG. 11a



State with no fractional delay

FIG. 11b



State with fractional delay 1/2

FIG. 11c

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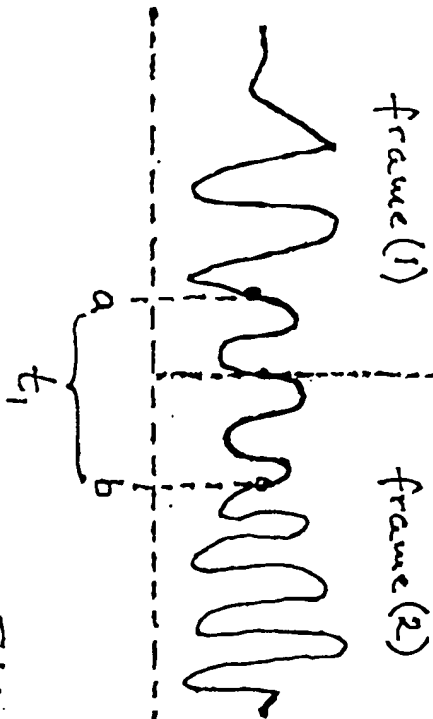


FIG. 12a

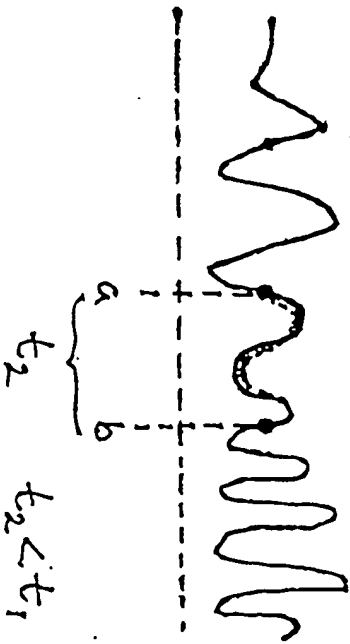


FIG. 12b

0949393-020400